Electronic Acknowledgement Receipt FES ID: 1242861 Application Number: 10810235 Confirmation Number: 9456 Apparatus and method for decreasing the latency between an Title of Invention: instruction cache and a pipeline processor First Named Inventor: James N. Dieffenderfer Customer Number: 25299 Filer: Larry Joe Hume/Amanda Childs Filer Authorized By: Larry Joe Hume Attorney Docket Number: BPS920030173US1 Receipt Date: 09-OCT-2006 Filing Date: 26-MAR-2004 Time Stamp: 16:34:34 Application Type: Utility International Application Number:

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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for status of Application	20421-00072-US_Status_Inq uiryPOA.pdf	59433	no	1

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/IDO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filling Receipt, in due course.